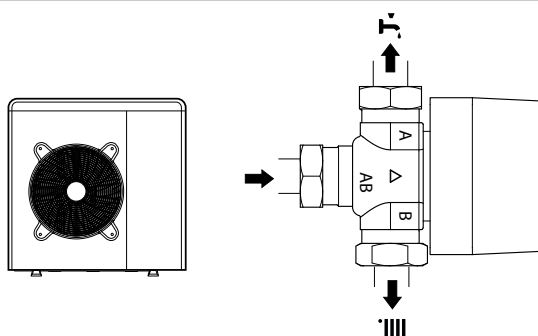
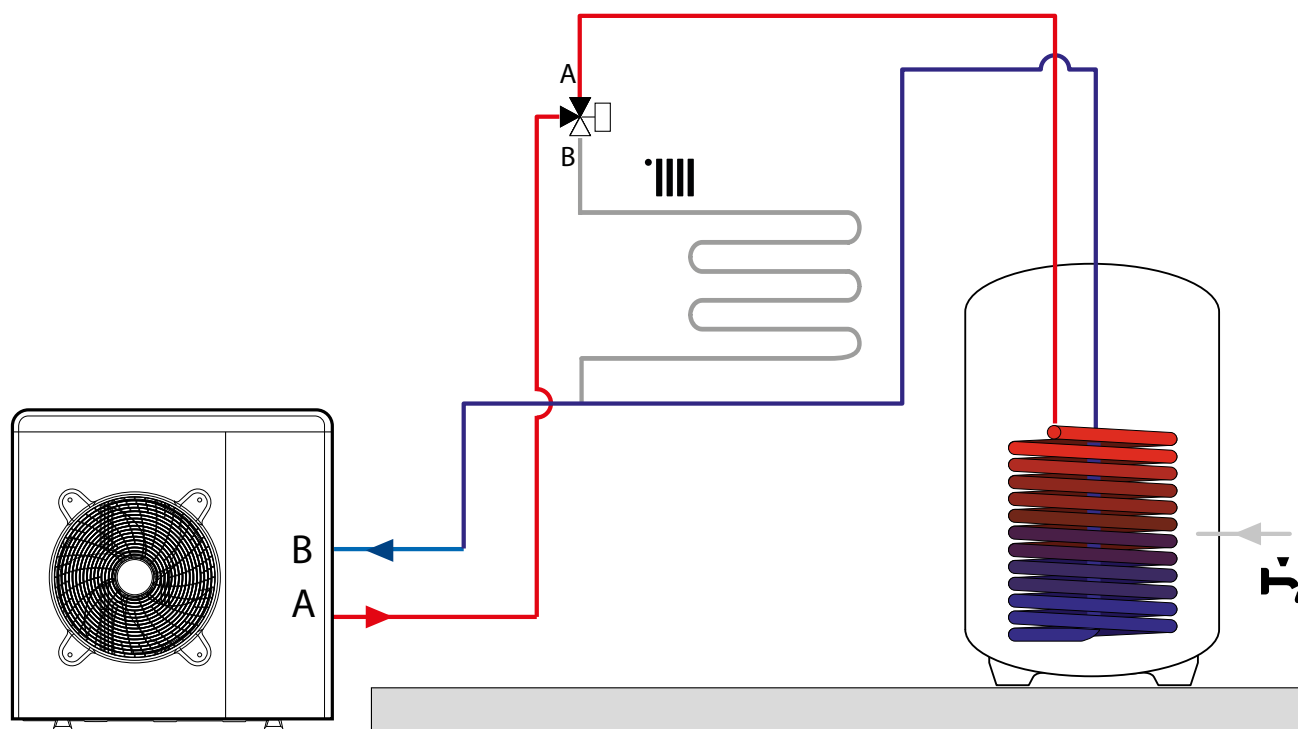
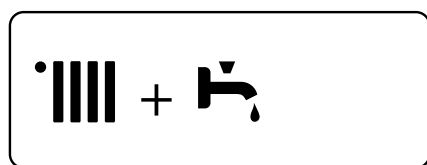
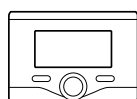
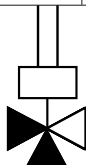
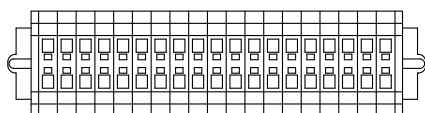
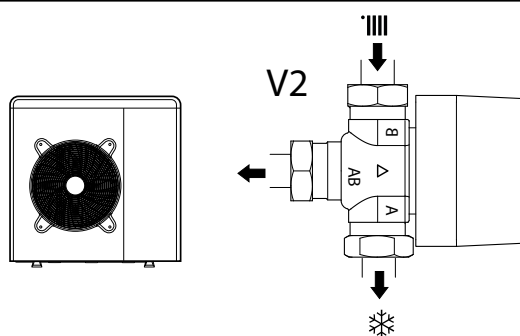
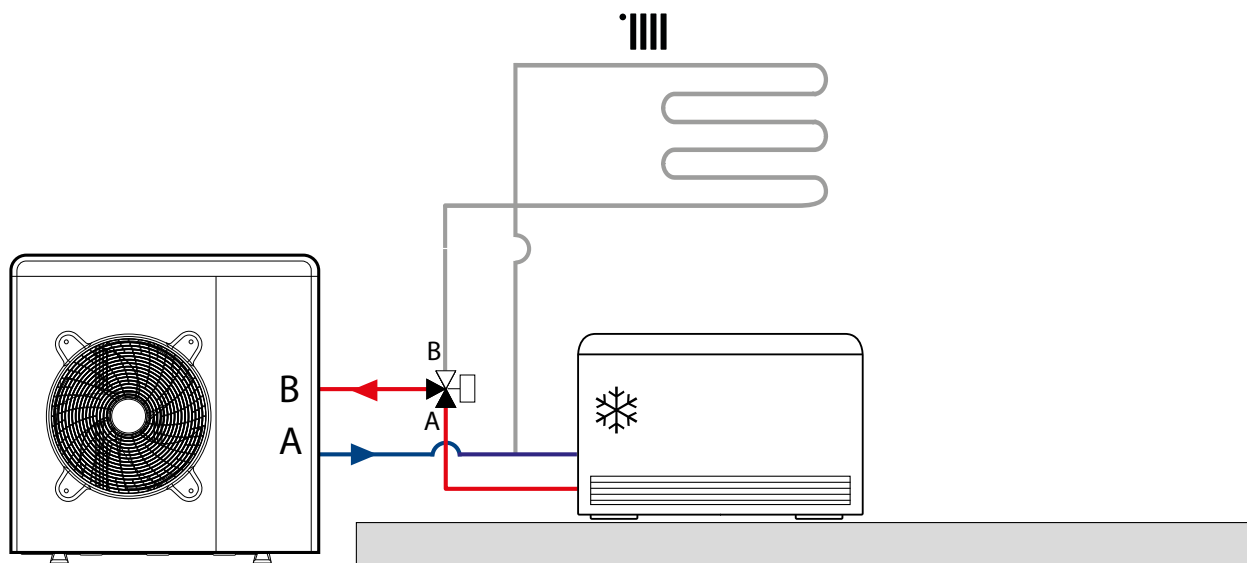
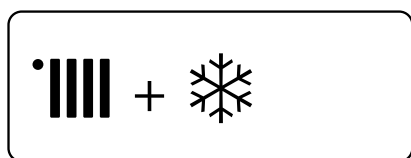


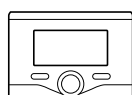
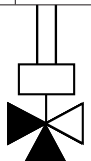
| HV<br>IN 3 | HV<br>IN 1 | HV<br>IN 2 | OUT<br>AUX 1 | ST 1 | PM AUX<br>N ⊕ L | V 1<br>⊕ N L | V 2<br>⊗ N ⊕ |
|------------|------------|------------|--------------|------|-----------------|--------------|--------------|
|------------|------------|------------|--------------|------|-----------------|--------------|--------------|



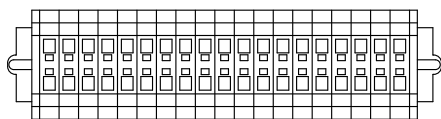
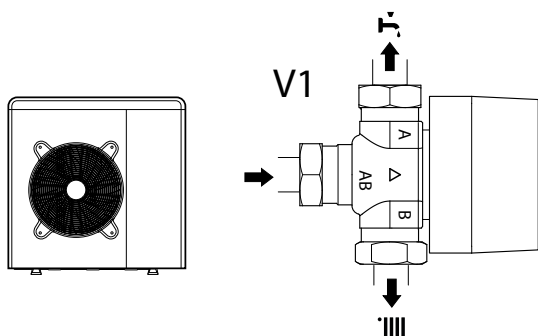
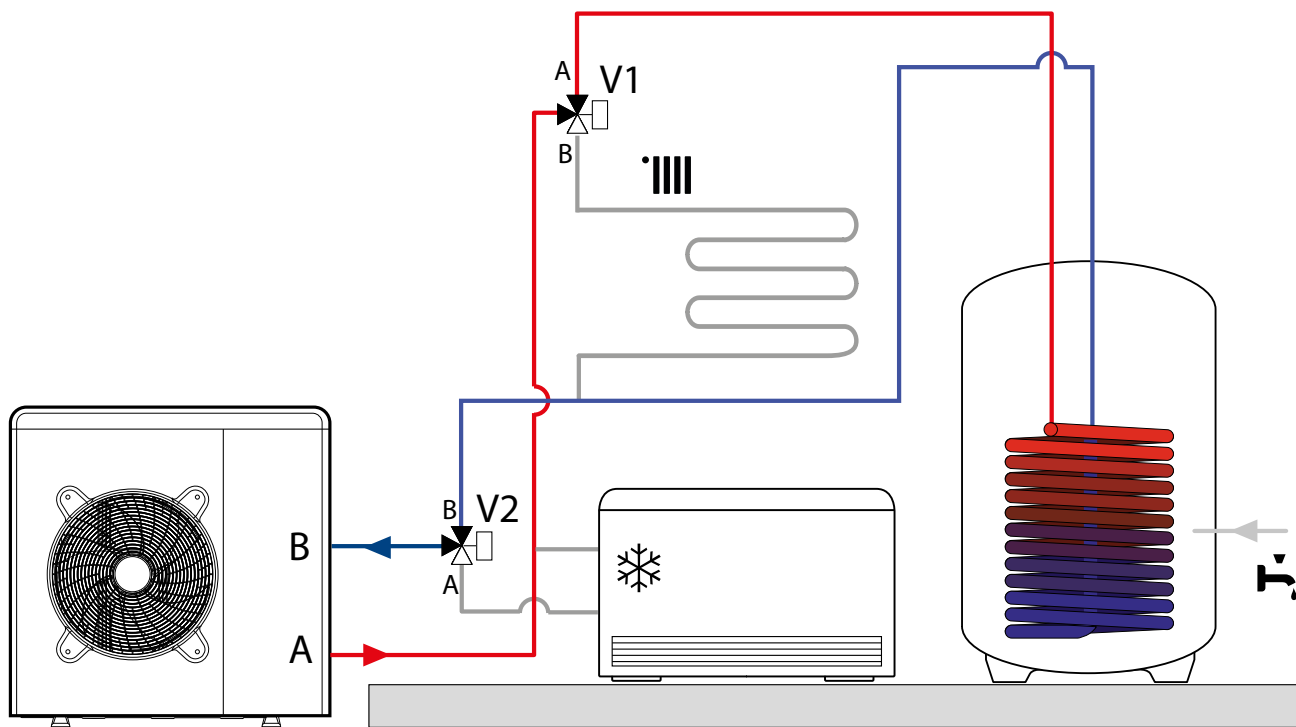
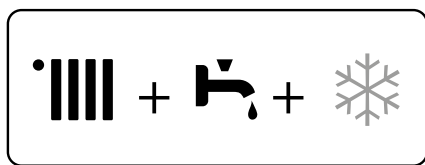
Par. 17.5.2 ≠ 0  
Par. 17.5.4 = 1



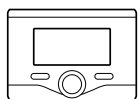
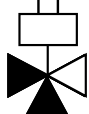
|            |            |            |              |      |                 |              |                 |
|------------|------------|------------|--------------|------|-----------------|--------------|-----------------|
| HV<br>IN 3 | HV<br>IN 1 | HV<br>IN 2 | OUT<br>AUX 1 | ST 1 | PM AUX<br>N ⊕ L | V 1<br>⊕ N L | V 2<br>⊗ N 'III |
|------------|------------|------------|--------------|------|-----------------|--------------|-----------------|



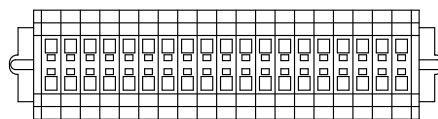
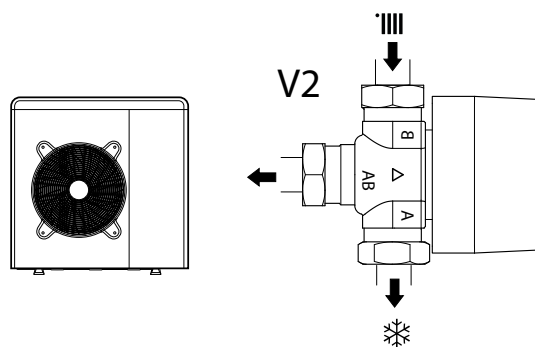
Par. 17.4.0 = 1



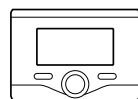
| HV   | HV   | HV   | OUT   | ST 1 | PM AUX | V 1   | V 2       |
|------|------|------|-------|------|--------|-------|-----------|
| IN 3 | IN 1 | IN 2 | AUX 1 |      | N ⊕ L  | ⊕ N L | * N ' III |



Par. 17.5.2 ≠ 0  
Par. 17.5.4 = 1  
Par. 17.4.0 = 1

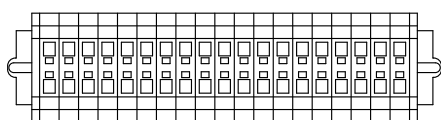
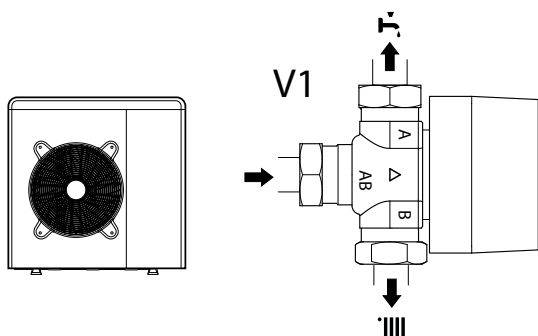
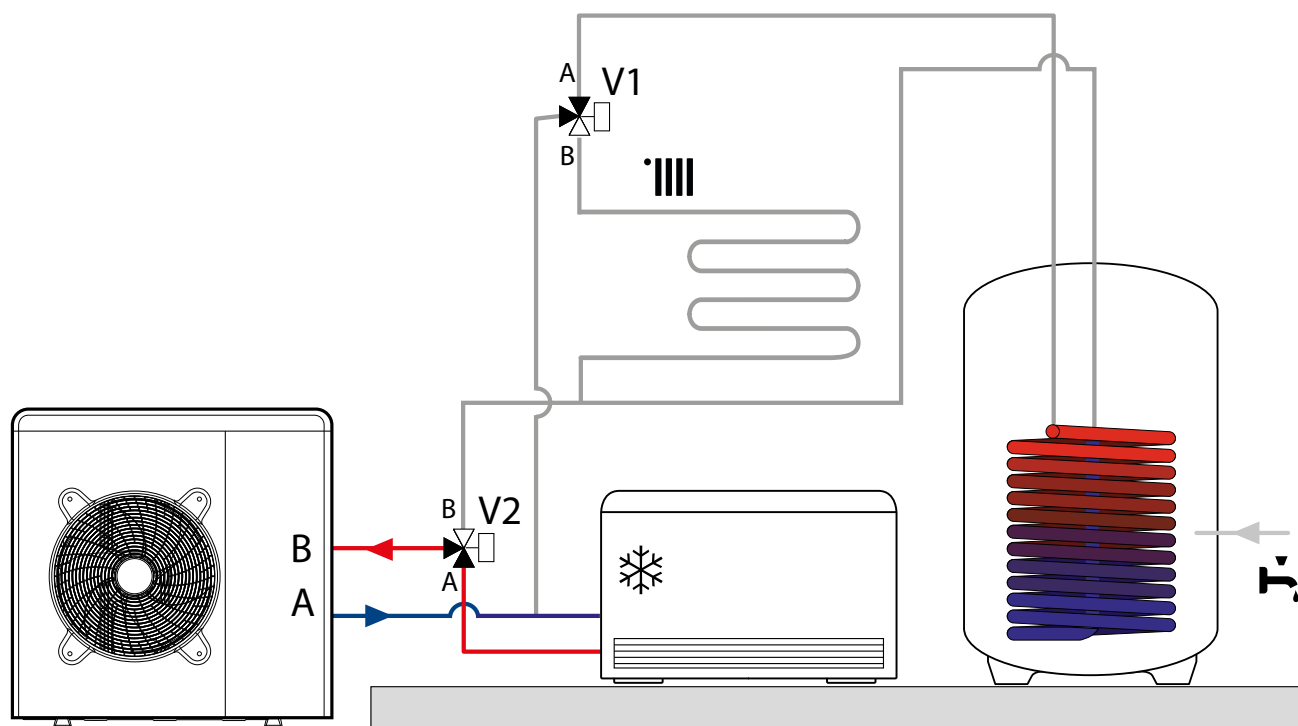
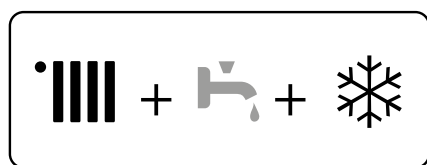


| HV   | HV   | HV   | OUT   | ST 1 | PM AUX | V 1   | V 2       |
|------|------|------|-------|------|--------|-------|-----------|
| IN 3 | IN 1 | IN 2 | AUX 1 |      | N ⊕ L  | ⊕ N L | * N ' III |

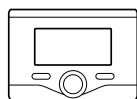


Par. 17.5.2 ≠ 0  
Par. 17.5.4 = 1  
Par. 17.4.0 = 1

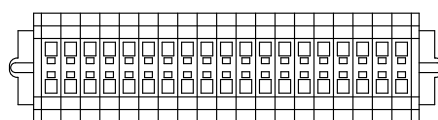
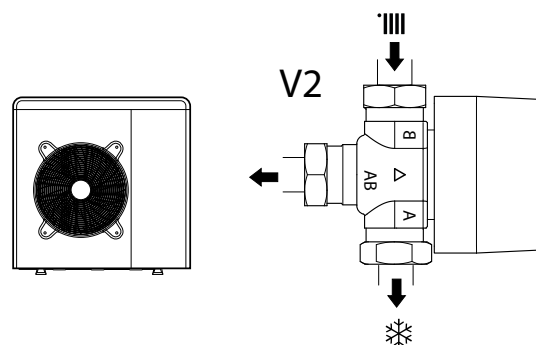
# 3WV FOR DHW OR COOLING 3078156



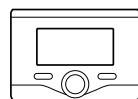
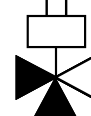
| HV   | HV   | HV   | OUT   | ST | PM AUX | V1  | V2  |
|------|------|------|-------|----|--------|-----|-----|
| IN 3 | IN 1 | IN 2 | AUX 1 | 1  | N      | N L | N L |



Par. 17.5.2  $\neq$  0  
Par. 17.5.4 = 1  
Par. 17.4.0 = 1



| HV   | HV   | HV   | OUT   | ST | PM AUX | V1  | V2  |
|------|------|------|-------|----|--------|-----|-----|
| IN 3 | IN 1 | IN 2 | AUX 1 | 1  | N      | N L | N L |



Par. 17.5.2  $\neq$  0  
Par. 17.5.4 = 1  
Par. 17.4.0 = 1